



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#2/a
mm
6702

Applicant(s): Deng, et al.

Attorney Docket No.: TI-33539

Application No.: 10/085,413

Group Art Unit: TBD

Filed: 2/28/2002

Examiner: TBD

Title: Enhanced Universal Serial Bus (USB) Bus Monitor Controller

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify, that on this date, this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231

Nancy J. Todd 4/4/02
Nancy J. Todd Date

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Please amend this application as follows:

In the Specification:

Page 6, lines 14-16

RECEIVED

APR 22 2002

Technology Center 2100

al
Figures 5a, 5b, and 5 c illustrate a detailed view of a debouncing logic circuit and a storage circuit for storing states on signal lines according to a preferred embodiment of the present invention;